

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	118658	((input near output) input/output I/O) near (structure configur\$5 layout circuit\$1 device\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:34
L2	0	1 and ((only near one near ((input near output) I/O) near pad) with enabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 16:59
L3	4	1 and ((one near ((input near output) I/O) near pad) with enabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:03
L4	12717	257/202,203,208,459,676,690,779-784,786. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:09
L5	964	4 and ((input near output) input/output I/O) near (pad\$1 structure configur\$5 layout circuit\$1 device\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:10
L6	304	5 and standard\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:11
L7	210	6 and ((bond\$3 chip die) near pad\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:11
L8	91	7 and enabl\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:13
L9	9	("5026664" "5172471" "5567655" "5719448" "6002163" "6060683" "6204074" "6207548" "6287893").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/14 17:20

L10	1	(((((input near output) input/output I/O) near (structure configur\$5 layout circuit\$1 device\$1)) AND (chip die) AND standard\$1 AND (external near (circuit device)) AND (conduct\$3 wir\$3) AND pad\$1 AND enabl\$3).CLM.	US-PGPUB; USPAT	OR	ON	2005/07/14 17:39
S1	2016	(AGP (Accelerated near Graphic Port)) with (PCIE (Peripheral near Component near Interconnect\$3 Express))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 10:58
S2	200	S1 and (((input near output) I/O) with (chip die))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 09:35
S3	11	S2 and ((chip die) near pad\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:11
S4	9	S1 and (((input near output) I/O) with ((chip die) near pad\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 09:45
S5	5	(AGP (Accelerated near Graphic near Port)) with (PCIE (Peripheral near Component near Interconnect\$3 near Express))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 09:44
S6	14	(AGP (Accelerated near Graphic near Port)) and (PCIE (Peripheral near Component near Interconnect\$3 near Express))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 09:44
S7	1129	(((((input near output) I/O) with ((chip die) near pad\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 10:57
S8	840	S7 and (first with second)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 09:46

S9	801	S8 and (conduct\$3 wir\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:13
S10	310	S9 and standard\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:13
S11	310	S10 and (die chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 09:48
S12	57	S11 and (enabl\$3 with disabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:02
S13	1	((input near output) I/O) AND ((chip die) near pad\$1) AND standrad\$1 AND (conduct\$3 wir\$3) AND (enabl\$3 disabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:45
S14	1	((input near output) I/O) AND ((chip die) near pad\$1) AND standrad\$1 AND (conduct\$3 wir\$3) AND (enabl\$3 disabl\$3)".CLM"	US-PGPUB; USPAT	OR	ON	2005/07/01 09:58
S15	66	(first near ((input near output) I/O) near pad) WITH (second near ((input near output) I/O) near pad)	US-PGPUB; USPAT	OR	ON	2005/07/01 10:24
S16	7352	((input near output) I/O) near (structure configur\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:12
S17	7352	((input near output) input/out/put I/O) near (structure configur\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 10:49
S18	245	S17 and ((chip die) near pad\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:21

S19	240	S18 and (conduct\$3 wir\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:13
S20	127	S19 and standard\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:13
S21	10	S17 and ((first near standard) with (second near standard))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 10:45
S22	8	(first near ((input near output) I/O) with (first near standard)) AND (second near ((input near output) I/O) with (second near standard))	US-PGPUB; USPAT	OR	ON	2005/07/01 10:25
S23	33166	((input near output) I/O) with (enabl\$3 disabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:23
S24	20	S23 and ((first near standard) with (second near standard))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:28
S25	31317	((input near output) input/output I/O) with enabl\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:27
S26	16	S25 and ((first near standard) with (second near standard))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:25
S27	573	((((input near output) input/output I/O) near pad\$1) with enabl\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:28

S28	0	S27 and ((first near standard) with (second near standard))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:28
S29	9	((input near output) input/output I/O) near pad\$1) with enabl\$3 with standard\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/01 13:28
S30	118658	((input near output) input/output I/O) near (structure configur\$5 layout circuit\$1 device\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 16:54
S31	8776	((input near output) input/output I/O) near (contact\$3 connect\$3 pad) near (structure configur\$5 layout circuit\$1 device\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 10:56
S32	687	((input near output) I/O) near (pad\$1 contact\$3 connect\$3)) with ((chip die) near pad\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 10:57
S33	30199	(AGP (Accelerated near Graphic Port)) and (PCIe (Peripheral near Component near Interconnect\$3 Express))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:03
S34	9	S33 and S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:01
S35	333	S33 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:01
S36	102	S35 and (enabl\$3 with disabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:13

S37	4438	(AGP (Accelerated near Graphic near Port)) and (PCIE (Peripheral near Component near Interconnect\$3 Express))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:03
S38	14	(AGP (Accelerated near Graphic near Port)) and (PCIE (Peripheral near Component near Interconnect\$3 near Express))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:03
S39	35	S32 and (enabl\$3 with disabl\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:11
S40	225	S32 and standard\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 17:11
S41	4	("5410254" "5712753" "5715127" "5818086").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/14 11:45
S42	159	S32 and (only near one)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/07/14 11:53
S43	28	("4685033" "5019889" "5036163" "5060037" "5063429" "5075753" "5095356" "5155065" "5182629" "5217916" "5291043" "5347150" "5420447" "5514895" "5545923" "5548135" "5604161" "5654588" "5689133" "5691568" "5713037" "5719944" "5734921" "5737587" "5773856" "5894565" "5926353" "6002268").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/14 14:22
S44	12	("5153507" "5279975" "5391892" "5457400" "5483175" "5514912" "5657284" "5679609" "5680344" "5684809" "5716218" "5721496").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/14 15:06